PONDICHERRY ENGINEERING COLLEGE, PUDUCHERRY – 605 014

CURRICULUM AND SYLLABI FOR AUTONOMOUS STREAM

M.TECH. (ELECTRICAL DRIVES AND CONTROL) COURSES

(FOR STUDENTS ADMITTED FROM ACADEMIC YEAR 2015-16 ONWARDS)

CURRICULUM

I SEMESTER

Subject	Subjects	Catagory	P	Period	s		Marks		Credits
Code	Subjects	Category	L	Т	Р	СА	SE	тм	Creatis
MA156	Mathematics	ΤY	3	1	0	40	60	100	4
EE151	Modern Control Theory	ΤY	3	1	0	40	60	100	4
EE152	Power Electronic Circuits and Systems	ΤY	3	1	0	40	60	100	4
EE153	Solid State Controlled Electrical Drives	ΤY	3	1	0	40	60	100	4
	Elective-I	ΤY	3	1	0	40	60	100	4
	Elective-II	ΤY	3	1	0	40	60	100	4
EE154	Solid State Systems Laboratory	LB	0	0	3	60	40	100	2
Total Credits									26

II SEMESTER

Subject	Subjects	Catagory	P	Period	s		Marks		Credits
Code	Subjects	Category	L	Т	Ρ	CA	SE	тм	Creaits
EE155	Digital Control and Controller Design	ΤY	3	1	0	40	60	100	4
EE156	Vector Controlled AC Drives	TCM	3	0	2	50	50	100	4
	Elective-III	ΤY	3	1	0	40	60	100	4
	Elective-IV	TY	3	1	0	40	60	100	4
	Elective-V	ΤY	3	1	0	40	60	100	4
	Elective-VI	TY	3	1	0	40	60	100	4
EE157	Electrical Drives Laboratory	LB	0	0	3	60	40	100	2
EE158	Research Methodology	PR	0	0	3 100 - 100		100	1	
Total Credits								27	

III SEMESTER

Subject	Subjects	Catagony	Periods				Credits		
Code	Subjects	Category	L	Т	Ρ	CA	SE	ТМ	Credits
EE159	Project Phase - I	PR				150	150	300	9
Total Credits							9		

IV SEMESTER

Subject	Subjects	Category	P	Perio	ds		Marks		Credits
Code	Subjects	Category	L	Т	Ρ	CA	SE	ТМ	Creatis
EE160	Project Phase - II	PR				200	200	400	14
	Professional Development Courses (Two One Credit Courses)	PR				200		200	2
	Total Credits							16	

A representative list of processional development courses is given below:

- a) Industrial training (Limited to one credit)
- b) Specific field knowledge training (Limited to a maximum of two credits)
- c) Seminar related with directed study (Limited to a maximum of two credits)
- d) Paper presentation in SCI journals (Limited to one credit)

#CA-Continuous Assessment, SE-Semester examination, TM-Total marks, *TY-Theory, TCM=Theory with mini project, LB-Laboratory, PR-Practice

LIST OF ELECTIVES

Sl. No.	Subject Code	Subjects	Category
1	EEE51	Adaptive Control Theory	TY
2	EEE52	Advanced Digital Signal Processing	TY
3	EEE53	Diagnosis and Protection for Solid State Systems	TY
4	EEE54	Embedded Systems	TY
5	EEE55	Flexible AC Transmission System Controllers	TY
6	EEE56	FPGA Based System Design	TY
7	EEE57	Fuzzy Control	TY
8	EEE58	Modern Power Electronic Converters	TY
9	EEE59	Neural Networks	TY
10	EEE60	Nonlinear Control Systems	TY
11	EEE61	Optimal Control Theory	TY
12	EEE62	Power Electronics in Power Systems	TY
13	EEE63	Power Quality	TY
14	EEE64	Wind Energy Conversion Systems	TY
15	EEE65	Special Electrical Machines	ΤY

SYLLABUS (Core Subjects)

Department :	Mathematics		Program	nme: M	.Tech. (I	Electrical [Drives a	nd Con	trol)
Semester : (One		Catego	ту : Т	Y				
Subject Code	Subject Norma		Ηοι	urs / We	eek	Credit	Max	imum N	/larks
Subject Code	Subject Name		L	Т	Р	С	СА	SE	ТМ
MA156	Mathematics		3	1	0	4	40	60	100
Prerequisite									
	 To int 	roduce LPP ideas							
Objectives	 To far 	niliarize the students wit	h ideas of Lin	ear alge	bra				
	 To int 	roduce stochastic proble	m						
	 Under 	stand LPP							
Outcome	 Under 	stand vector space and t	transformatio	n can al	ole to ap	ply rando	m proc	ess	
UNIT – I									
Vector spaces	, subspaces, span of a set, linear independence and dependence, Dimension and Bases,							, inner	
product spaces	s - Gram-Schmig	It orthogonalization.							
UNIT – II	Linear Transfo	ormations			Hou	ırs: 9			
Definition and	examples, Ran	ge and Kernel of a linea	ar map, rank	and nul	lity, Inv	erse of a	linear t	ransfor	mation,
consequences	of Rank-Nullity	theorem, the space L (U, V), compo	sition o	f linear	maps, Ma	trix ass	ociated	with a
linear map and	l linear map ass	ociated with a matrix.	-			-			
UNIT – III	Linear Progra	mming			Ηοι	ırs: 9			
Basic concepts	- Graphical and	d Simplex methods - Big	M-techniques	s – Two	Phase m	ethods.			
UNIT – IV	Dynamic and	Quadratic Programming			Ηοι	ırs: 9			
Dynamic Progr	ramming – Solu	itions of Problems using	g dynamic pro	ogramm	ing tech	nniques –	Definit	ions of	convex
programming -	- Kuhn Tucker co	onditions – Quadratic Pro	ogramming –	Wolf's I	Method.				
UNIT – V	Random Proc	ess			Ηοι	ırs: 9			
Stochastic Prod	cess – Classifica	tion of Stochastic proces	s - Poisson pr	ocess -0	Gaussiar	process -	Marko	v chain	s - Auto
correlation - C	ross correlation								
Total contact H	lours: 45	Total Tutorials: 15	Total Prac	tical Cla	sses:	Tot	al Houi	rs: 60	
Text Books:									
1. V. Kris	hnamurthy, V.P	. Maiwa and J.L.Arora, "	An Introductio	on to Lir	ear Alg	ebra", Affi	liated E	ast We	st Press
Pvt. Lte	d., New Delhi.								
2. H.A.Ta	ha, "Operations	s Research – An Introduc	tion", Mac M	illian Pu	blishing	Co., 1982	•		
Reference Boo	oks:								
1. J.C.Par	nt, "Optimizatio	n and Operations Resear	ch", Jain Publ	lishers,	New De	hi.			
2. Kishor	e S Trivedi, "Pro	bability and Statistics w	ith Reliability	, Queuir	ng and C	Computer	Science	e Applica	ations",
John W	Viley & Sons, 20	02.							

• ·	Electrical and Electronics Engineering				. (Electrical Dr	ives and	control)
Semester : (Dne	Catego	/	TY				
Subject Code	Subject		rs / W		Credit		imum N	1
-	-	L	T	P	C	CA	SE	TM
EE151	Modern Control Theory	3	1	0	4	40	60	100
Prerequisite								
	 The modeling of dynamic system 		•					
	To analyze the dynamic system	for contr	ollabil	ity and	observability			
Objectives	The design of pole placement co	ontroller	and st	ate obs	ervers			
	 The stability of dynamic system 	s using Li	apuno	v metho	bc			
	To formulate the optimal control	ol proble	ms and	l solve t	hem			
	Model any dynamic system usir	ng state s	pace a	pproach	า			
	Analyze the dynamic systems for	-				ability		
Outcome	 Design controller and observer 					,		
	 Solve optimal control problems 				•			
		anu uesi	gnopt					
UNIT – I	Introduction to State Space Approach		du co co tra	and of	Hours: 9			
	hysical systems using state space appro I. State diagram, state space and state tra			-	•	• •		
	Jordan canonical forms – Similarity			•			-	
-	blution of Linear Time Invariant (LTI) sta							
computational		ic cquu					is prope	in the s
UNIT – II	State Space Analysis				Hours: 9			
	nd Eigen vectors – Cayley Hamilton the	orem –	minim	al polvr		ot – Con	trollabil	itv and
-	- Tests – Kalman decomposition techniqu			. ,	·			,
UNIT – III	State Feedback Controller Design				Hours: 9			
	State Feedback Controller Design ign by state feedback –Necessary and	sufficie	ent co	ndition		pole pla	cement	- state
Controller des					forarbitrary	• •		
Controller des regulator prob Observer Desig	ign by state feedback –Necessary and	eedback	with i	ntegral	forarbitrary control. Eiger	structu	re assig	nment
Controller des regulator prob Observer Desig principle.	ign by state feedback –Necessary and lem. Tracking (Servo) problem – State f gn – Full order/reduced order observer d	eedback	with i	ntegral	forarbitrary control. Eiger I state feedba	structu	re assig	nment
Controller des regulator prob Observer Desig principle. UNIT – IV	ign by state feedback –Necessary and lem. Tracking (Servo) problem – State fo gn – Full order/reduced order observer d Stability Analysis	eedback esign – c	with in bserve	ntegral er basec	forarbitrary control. Eiger d state feedba Hours: 9	n structu ick contro	re assig ol – sep	nment aratio
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Controller des regulator prob Observer Desig principle. UNIT – IV Stability conce local and globa	ign by state feedback –Necessary and lem. Tracking (Servo) problem – State f gn – Full order/reduced order observer d Stability Analysis pts – BIBO Asymptotic stability - stability I stability – Lyapunov stability analysis - k	eedback esign – c y definiti	with in observe	ntegral er based state sj	forarbitrary control. Eiger d state feedba Hours: 9 pace domain	n structu ick contro	re assig ol – sep	nment aratio
Controller des regulator prob Observer Desig principle. UNIT – IV Stability conce local and globa UNIT – V	ign by state feedback –Necessary and lem. Tracking (Servo) problem – State for gn – Full order/reduced order observer d Stability Analysis pts – BIBO Asymptotic stability - stability I stability – Lyapunov stability analysis - k Optimal Control	eedback esign – c y definiti Krasovski	with in bserve ons in i Meth	ntegral er basec state sj od.	forarbitrary control. Eiger d state feedba Hours: 9 pace domain - Hours: 9	structu ck contro – stabilit	re assig ol – sep y theore	nment aration ems of
Controller des regulator prob Observer Desig principle. UNIT – IV Stability conce local and globa UNIT – V Linear quadrat	ign by state feedback –Necessary and lem. Tracking (Servo) problem – State for gn – Full order/reduced order observer d Stability Analysis pts – BIBO Asymptotic stability - stability I stability – Lyapunov stability analysis - K Optimal Control ic optimal regulator (LQR) problem formu	eedback esign – c y definiti Krasovski ulation –	with in observe ons in i Meth optima	ntegral er basec state sj od. al regula	forarbitrary control. Eiger d state feedba Hours: 9 pace domain Hours: 9 ator design by	- stabilit	re assig ol – sep y theore ter adju	nment aration ems of stmen
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•	Electrical and El	ectronics Engineering				h. (Electrio	cal Drive	es and C	ontrol)
Semester :	One		Categ	ory	: TY	•			
Subject Code	Subject			urs / W	1	Credit		imum N	1
-		nio Circuito and Customer	L	T	P	C	CA	SE	TM
EE152	Power Electro	onic Circuits and Systems	3	1	0	4	40	60	100
Prerequisite									
Objectives		rn and design natural and f		nutated	Power	converte	rs		
		rn various switching techni	-			· · ·		-	
Outcome		o design and analyze the po			•		ronics ci	rcuits.	
		nstruct improved converter	topologies	for spe					
UNIT – I		nutated Converters				urs: 9		<u> </u>	
	-	controlled rectifier – opera	-			-		-	
		with and without freewh	-				•		
•		mit - performance at input					Load co	mmutat	ed and
		ns – various modes, Dual co	nverters, hig	gner pu					
UNIT – II		ter and Voltage Controller				urs: 9			<u> </u>
• •	•	oconverters - three phase	• •		•		•	•	
· ·	•	ut voltage and frequency	-		•	-			
-		to bi-directional switches				-		-	
		se angle range – per unit	plot for vari	ious io	aus - II	iput and	ουτρυτ	benom	lance -
UNIT – III	ments – harmor	utated Converters			Ца	ırs: 9			
			od principlo	volta				tod cho	nnore
	•	hoppers - forced commutat		-	-				• •
		t element – class A, B, C, D hase forced commutated ii	-	-		-			-
		lesign principles.	iverter – vo	itage st	Juice –	current s	ource -	anaiysis	WILLI
UNIT – IV	Modulation T				Но	ırs: 9			
		ole of operation of half and	full bridge i	invorto			naram	ators - 1	Voltag
- ·		iques – square wave– SPM	-				•		-
		tching operation – perfor	•	•	•				
-	• •	120 degree conduction mo		•		•			•
•	-	ers - Voltage control techn							-
power.			4						0
UNIT – V	High Power A	pplications			Ηοι	ırs: 9			
High power co		er pulse operation - series	connected	– para	lel con	nected co	nverter	s - high	powe
inverters - pha	se shifted oper	ration – parallel connected	 cascaded 	connec	ted inv	/erters – i	nverter	s with/\	vithou
transformer –	Jesign of high p	ower converters and inver	ters. Introdu	uction t	o multi	level inve	rters - o	liode cla	amped
flying capacito	r, cascade type	multilevel inverters -compa	arison of mu	ltilevel	inverte	rs – appli	cations		
Total contact I	Hours: 45	Total Tutorials: 15	Total Practi	cal Clas	ses:	Tot	tal Hour	s: 60	
Text Books:	ngh and K.B.Kl	hanchandani, "Power Elec	tronics", Tat	ta McG	raw-Hi	ll Publish	ing Con	npany L	imited
Text Books: 1. M.D.S	ingh and K.B.Kl Delhi, 2003.	hanchandani, "Power Elec	tronics", Tat	ta McG	raw-Hi	ll Publish	ing Con	npany L	imited
Text Books: 1. M.D.Si New D	elhi, 2003.	hanchandani, "Power Elec Electronics Circuits, Devices					-		
Text Books: 1. M.D.Si New D 2. M.H.F	elhi, 2003. Rashid, "Power I		and Applica	itions",	Prentio	ce Hall Inc	-		
Text Books: 1. M.D.Si New D 2. M.H. F 3. 3. Mo	pelhi, 2003. Rashid, "Power I han, Underland	Electronics Circuits, Devices	and Applica	itions",	Prentio	ce Hall Inc	-		
Text Books: 1. M.D.Si New D 2. M.H.F 3. 3. Mo Reference Boo	velhi, 2003. Rashid, "Power I han, Underland bks:	Electronics Circuits, Devices	and Applica ronics", John	ntions", n Wiley	Prentio &Sons	ce Hall Inc	-		
Text Books: 1. M.D.Si New D 2. M.H. F 3. 3. Mo Reference Boo 1. Cyril Li 2. Derek	velhi, 2003. Rashid, "Power I han, Underland oks: ander, "Power E A. Paice, "Pow	Electronics Circuits, Devices and Robbins, "Power Elect	and Applica ronics", Johi ernational E	ntions", n Wiley dition,	Prentio &Sons 1993.	ce Hall Inc , 1995	lia, New	Delhi, 2	2004.
Text Books: 1. M.D.Si New D 2. M.H. F 3. 3. Mo Reference Boo 1. Cyril Li 2. Derek IEEE P	velhi, 2003. Rashid, "Power I han, Underland oks: ander, "Power E A. Paice, "Pow ress, 1999.	Electronics Circuits, Devices and Robbins, "Power Elect Electronics, McGraw Hill Int er Electronics Converter H	and Applica ronics", Johr ernational E armonics: N	ntions", n Wiley dition, 1ulti-pu	Prentio &Sons 1993. Ise Me	thods for	lia, New	Delhi, 2 Power",	2004. Wiley
Text Books: 1. M.D.Si New D 2. M.H. F 3. 3. Mo Reference Boo 1. Cyril Li 2. Derek IEEE Pl 3. D.Graf	pelhi, 2003. Rashid, "Power I han, Underland oks: ander, "Power E A. Paice, "Pow ress, 1999. name Holmes,	Electronics Circuits, Devices and Robbins, "Power Elect Electronics, McGraw Hill Int er Electronics Converter H Thomas A. Lipo, Pulse W	and Applica ronics", Johr ernational E armonics: N	ntions", n Wiley dition, 1ulti-pu	Prentio &Sons 1993. Ise Me	thods for	lia, New	Delhi, 2 Power",	2004. Wiley
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Comester		ectronics Engineering				LICUIU	Drives and	Contro	'/
Semester :	One		Catego	-	TY	a 111			
Course Code	Course Name		Нос	ırs / W T	еек Р	Credit C		imum N SE	Viarks TM
55450	Solid State Co	ontrolled Electrical	3	1	0	4	40	60	100
EE153	Drives								
Prerequisite:								1	
	• To learn con	verter and chopper cont	rol of dc o	drives					
Objectives:	• To learn the	concept of closed loop of	control of	AC and	DC driv	ves			
	• To learn abo	ut digital control of driv	es						
	 Ability to det 	ermine the characterist	ics of driv	es					
Outcome:	 Ability to des 	sign converter fed dc dri	ves and cl	nopper	fed dc o	drives			
	 Ability to des 	sign of closed loop contr	ol of drive	es					
UNIT – I	Speed Contro					Hours: 9			
	•	ments - typical load to	• •			0, 0		•	
•	•	load type and duty ratio	o - motor	choice	- speed	control pri	nciples - co	nstant t	orque
	r – multi quadra	•							
		or - converter fed - chop	•	•	-	-	-	•	
	 speed rever 	sal - braking -regenera	tion - clos	ed loo	p regul	ation - Inc	hing – jogg	ing – e	ffect o
saturation.	-								
UNIT – II		troller and Converter fo				Hours: 9	_		
		regulation – speed loc							
		ut braking and regener							
	-	otors, converters, chopp				speed cont	roller, curre	ent cont	roller
		without current contro		ilation.					
UNIT – III	•	of Induction Motor-St	ator Side			Hours: 9			
							1		
•	•	wer controllers – princi	• •				-		•
operation – co	nstant flux ope	ration - constant power	operatio	n – spe	ed cont	rol of VSI a	and CSI fed	drives -	desig
operation – co examples. Clos	nstant flux ope sed loop contro	ration - constant power I schemes - dynamic ar	operatio	n – spe	ed cont	rol of VSI a	and CSI fed	drives -	desig
operation – co examples. Clos waveforms - e	nstant flux ope sed loop contro ffect of non- sin	ration - constant power l schemes - dynamic ar usoidal supply.	operation nd regene	n – spe	ed cont	rol of VSI a - speed re	and CSI fed	drives -	- desig
operation – co examples. Clos waveforms - e UNIT – IV	nstant flux ope ed loop contro ffect of non- sin Speed Contro	ration - constant power l schemes - dynamic ar usoidal supply. of Induction Motor–Ro	operation nd regene	n – spe rative	ed cont braking	rol of VSI a - speed re Hours: 9	and CSI fed eversal – tra	drives - acing of	desig critic
operation – co examples. Clos waveforms - er UNIT – IV Torque slip ch	nstant flux ope sed loop contro ffect of non- sin Speed Contro aracteristics – s	ration - constant power I schemes - dynamic ar usoidal supply. of Induction Motor–Rc peed control through sl	operation nd regene ntor Side ip - rotor	n – spe rative resistai	ed cont braking nce con	rol of VSI a - speed re Hours: 9 trol- chopp	and CSI fed eversal – tra	drives - acing of ed resis	desig critic tance
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operation – co examples. Clos waveforms - er UNIT – IV Torque slip ch equivalent res stator voltage	nstant flux ope sed loop contro ffect of non- sin Speed Contro aracteristics – s istance – TRC s control and ro	ration - constant power I schemes - dynamic ar usoidal supply. of Induction Motor–Rc peed control through sl strategy – characteristic tor resistance control –	operation nd regene ntor Side ip - rotor c relation design so	n – spe rative resistar betwe olution	ed cont braking nce con en slip s – clos	rol of VSI a - speed re Hours: 9 trol- chopp and chopp ed loop co	and CSI fed eversal – tra per controllo per duty rate potrol scher	drives - acing of ed resis tio - co ne. Slip	tance
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operation – co examples. Clos waveforms - er UNIT – IV Torque slip ch equivalent res stator voltage recovery - torc solutions - clos UNIT – V Need for leadi control - torqu with various po Total contact I Text Books: 1. G.K.Dubey, ' 2. R.Krishnan, 2003. 3. J.M.D Murp Reference Boo 1. Bin Wu, "Hig 2. Buxbaum, A 3. Bimal K.Bos 2003.	nstant flux ope sed loop contro fect of non- sin Speed Control aracteristics – s istance – TRC s control and ro ue slip characte ed loop control Speed Control Speed Control of PF operatio te angle controllers Jours: 45 'Power semicon "Electric Motor hy, F.G.Turnbull ks: sh-Power Conve Schierau, and K e, "Modern Pov	ration - constant power I schemes - dynamic ar usoidal supply. of Induction Motor-Ro peed control through sl strategy – characteristic tor resistance control – eristics - power factor co scheme. of Synchronous Motor n - open loop VSI fed co - power factor control s - starting methods- bro Total Tutorials: 15 ductor controlled device Drives –Modeling, Anal , "Thyristor control of A	operation of regene otor Side ip - rotor relation design so onsideration Drives Irive – gro -simple d ush less ex Total F es", Prent ysis and C C motors, iley-IEEE F control sy Drives", P	n – spe rative l resista betwe blution ons – su Dup dri esign e ccitatio Practica ice Hall Control' Pergar Press. stems earson	ed cont braking nce con en slip s – clos ub and s ve appl example n syster I Classe I Interna ", Prent mon Pre for DC c Educat	rol of VSI a - speed re Hours: 9 trol- chopp and chopp ed loop cc uper synch Hours: 9 ications. Se s Closed Ic ns s: tional New ice-Hall of ss, Oxford, lrives", Spr	and CSI fed eversal – tra per controlle per duty rate ontrol scher aronous ope elf-control - pop speed of Total Hour v Jersey, 19 India Pvt. Li , 1988.	drives - acing of ed resis tio - co ne. Slip eration - - margi control s rs:60 89. td., Nev	- desig critic tance mbine powe - desig n ang schem v Dell
operation – co examples. Clos waveforms - er UNIT – IV Torque slip ch equivalent res stator voltage recovery - toro solutions - clos UNIT – V Need for lead control - torqu with various po Total contact I Text Books: 1. G.K.Dubey, 2. R.Krishnan, 2003. 3. J.M.D Murp Reference Boo 1. Bin Wu, "Hig 2. Buxbaum, A 3. Bimal K.Bos 2003. 4. Jean Bonal a	nstant flux ope sed loop contro fect of non- sin Speed Control aracteristics – s istance – TRC s control and roi ue slip characte ed loop control Speed Control ng PF operatio le angle control ower controllers tours: 45 'Power semicon "Electric Motor hy, F.G.Turnbull ks: gh-Power Conve Schierau, and K e, "Modern Pow	ration - constant power I schemes - dynamic ar usoidal supply. of Induction Motor-Ro peed control through sl strategy – characteristic tor resistance control – eristics - power factor co scheme. of Synchronous Motor n - open loop VSI fed co - power factor control s - starting methods- bro Total Tutorials: 15 ductor controlled device Drives –Modeling, Anal , "Thyristor control of A erters and AC Drives", W .Staughen, "A design of wer Electronics and AC	operation of regene otor Side ip - rotor relation design so onsideration Drives Irive – gro -simple d ush less ex Total F es", Prent ysis and C C motors, iley-IEEE F control sy Drives", P	n – spe rative l resista betwe olution ons – su oup dri esign e ccitatio Practica ice Hall Control' Pergar Press. vstems earson .avoisie	ed cont braking nce con en slip s – clos ub and s ve appl example n syster I Classe I Interna ", Prent non Pre for DC c Educat	rol of VSI a - speed re Hours: 9 trol- chopp and chopp ed loop cc uper synch Hours: 9 ications. Se s Closed Ic ns s: tional New ice-Hall of ss, Oxford, lrives", Spr ion (Singap	and CSI fed eversal – tra per controlle per duty rate ontrol scher aronous ope elf-control - pop speed of Total Hour v Jersey, 19 India Pvt. Li , 1988.	drives - acing of ed resis tio - co ne. Slip eration - - margi control s rs:60 89. td., Nev	- desig critic tance mbine powe - desig n ang schem v Dell

Department : Ele	ectrical and Electronics Engineering	Programme: M.Tech. (Electrical Drives and Control)								
Semester : Or	ne	Category : LB								
Subject Code	Subject	Hours / Week		Credit	Max	imum N	larks			
Subject Code	Subject	L T P		С	СА	SE	ТМ			
EE154	Solid State Systems Laboratory	-	-	3	2	60	40	100		
Prerequisite										
Objectives	To provide a practical understanding of electrical Drives.	some c	of the	concept	ts learnt in	the the	eory cou	irse on		
Outcome	The Students would have gained practica learnt in the electrical Drives.	al exper	ience	about s	some of the	e Theor	etical co	oncepts		
List of Experime	nts:									
Design and valida	ation of solid state systems choosing any ten	system	s as lis	sted bel	ow					
1. Firing Pu	lse Generation Schemes for Two Pulse and Si	x Pulse	Conve	erters						
2. Power Fa	actor Improvement Methods									
3. Higher-P	ulse Converters									
4. Thyristor	Controlled Reactor									
5. Thyristor	Switched Capacitor									

- 6. Thyristor Controlled Series Compensator
- 7. Three/Six Phase Delta Connected Thyristor Controlled Reactor
- 8. Static Tap Changing of Transformer
- 9. DC-DC Converters
- 10. Three Phase Voltage Source Inverter
- 11. Single Phase Sinusoidal PWM Inverter
- 12. Multi-Level Inverters

	Electrical and Electronics Engineering			M.Tech	. (Electrical	Drives and	Contro)
Semester :	Гwo	Categ	ory :	ΤY				
Subject Code	Subject	Ho	urs / W	eek	Credit	Max	imum N	/larks
Subject Code	Subject	L	Т	Р	С	CA	SE	ТМ
EE155	Digital Control and Controller Design	3	1	0	4	40	60	100
Prerequisite	Modern Control Theory							
	 Basics of digital control syster 	ns						
	 The modeling of digital control 	ol system	using st	ate spa	ce approac	า		
Objectives	The analysis of digital control	system fo	r contro	ollability	and obser	vability		
Objectives	 The design of controllers usin 	g classical	and sta	ate spac	e approach	es		
	 The stability of dynamic syste 	ms using l	Liapuno	v meth	od			
	 To formulate the optimal con 	trol proble	ems and	d solve t	them			
	 Understand the digital control 	l concepts	5					
	 Model any sampled data system 	em using s	state sp	ace app	roach			
Outcome	 Analyze the discrete time system 	tems for c	ontrolla	bility, o	bservabilit	y and stabil	ity	
outcome	 Design classical and pole plac 	ement cor	ntroller	and obs	server for s	ystems mo	deled in	state
	space							
	 Solve discrete time optimal control 	ontrol pro	blems					
UNIT – I	Introduction				Hours: 9			
	o discrete time control system - Sa							
	sampling theorem – data interpolatic	on – Z tra	ansform	–prop	erties - inv	erse Z tra	nsform	- Puls
transfer function					1			
UNIT – II	State Variable Technique				Hours: 9			
	ns of discrete time systems – solution		•				• •	
-	alization and state diagram – pulse tra				-		-	uation
-	igen vectors - Similarity transformation		rmatio	n into va		nical forms	•	
UNIT – III	Controllability, Observability and Sta	-			Hours: 9			
•	and observability of Llinear Time Invar				•			•
•	relationship between controllability,		•	•				OTLI
UNIT – IV	ystems - Jury's stability tests – Bilinear	transform	nation m	ietnoa ·	- Lyapunov Hours: 9	stability an	aiysis.	
-	Controller Design – I	ma racna	nco d	iroct dia		in 7 and W	Vinlana	lunda
	tween root locations in Z-plane and ti prm)PID controllers – proportional, inte	•		-			•	
	onventional tuning procedures – Read	-						
Ziegler Nichols			c mem			015 51001	inty me	
UNIT – V	Controller Design – II				Hours: 9			
	 Controller Design Design via pole placement – obser 	ver haser	1 state	feedba		ction to di	pital rec	lesign
	ollers – quadratic optimal control –stea						-	-
•	-Extended Kalman filter.	ing others i	1000.00			opennaro		
Total contact I		Total	Practica	l Classe	es:	Total Hou	rs:60	
Text Books:								
	a, "Discrete time control systems", Pea	rson Edu.	, 2003.					
-	in, Powell, workman, "Digital control of			s", Pear	son Edu., 2	002.		
	n M. Moudgalya, "Digital control", Joh	•	•					
3. Kanna								
3. Kanna Reference Boo	iks:							
Reference Boo	ks: al, "Digital Control and state variable n	nethods",	Tata M	cGraw H	Hill, New De	elhi, 2003.		
Reference Boo1.M.Gop							.td., 200)2

Department :	epartment : Electrical and Electronics Engineering Programme: M.Tech. (Electrical Drives and Control))	
Semester :	Two		Catego	ory :	тсм	-				
Subject Code	Subject		Ηοι	ırs / W	eek	Credit		Maxi	mum N	larks
-	-		L	Т	Р	С		CA	SE	ТМ
EE156		lled AC Drives	3	0	2	4		50	50	100
Prerequisite		ntrolled Electric Drives								
Objectives		ector control concepts								
-		r control techniques to va								
		rporate closed loop conti	rol techi	niques	using d	-q transfor	rmatic	on for	various	power
Outcome	converter appl									
		lop high performance clos		contro	oller for					
UNIT – I		elling of Induction Motor				Hours: 9				
		on machines- Dynamic d								
		ationary d ^s -q ^s frame to s	ynchron	ously r	otating	frame d°-	q° tra	nsform	nation. I	nverse
transformation		C C L L L								<i>c</i>
	-	reference frame model-K	•		•	•			•	
-	•	amic model state space e	•		eralized	i model in	arbitr	rary, st	ator and	i rotor
	_	netic torque and flux linka		tions.						
UNIT – II		Control of Induction Mot				Hours: 9				
		alar versus vector control							ام م مر خ م	
		rotor flux orientation, Flue				-	ier and	a curre	nt mod	21.
	-	d CSI, Direct vector contro								
UNIT – III	Indirect and Induction Mot	Stator Flux Oriented	vector	Contr	TOI OT	Hours: 9				
Indirect vector		r diagram, flux and slip sp	naad ast	imatio	n imple	mentation	fors	arvo d	rivo wit	h onen
	•	current control.	Jeeu est	intatio	n, imple		1101 3			ropen
		ector control of CSI fed dr	ives ver	tor cor	ntrol of	cycloconye	orter d	łrive		
UNIT – IV		l of Synchronous Motor E				Hours: 9				
		nronous machines; Vecto		l of Sv	nchron		s – Fie	eld wea	akening	mode:
	•	torque angle control, U							-	-
	-	control and flux weakenin								
UNIT – V	Direct Torque		-	· · ·		Hours: 9				
Direct torque a	and Flux Contro	I- Torque expression with	stator a	and rot	or fluxe	s, Control	strate	egy, DT	C of Ind	luction
motor using Di	rect self control	and space vector modula	ation.							
Total contact I	Hours: 45	Total Tutorials:	Total F	ractica	al Classe	es: 30	Tota	Hours	s: 75	
Text Books:										
1. Bimal	K.Bose, "Mode	rn Power Electronics and	AC Driv	es, Pea	rson Ed	lucation (S	ingap	ore) Lt	d., New	[,] Delhi,
2003.										
		otor Drives- Modeling, Ar	nalysis, a	ind Cor	ntrol, Pr	entice-Hal	l of In	idian P	rivate Li	mited,
	elhi, 2003.									
Reference Boo										
		r, "Vector Control of AC D	-							
	Novotny and T.	A.Lipo, "Vector control a	and dyn	amics	ot AC c	irives", Ox	tord S	Science	e Public	ations,
 1996. Paul, C, Krause, Oleg Wasynczuk and Scott D. Subhoff, "Analysis of Electric Machinery and Drive Systems" 										
	-	-	ubhoff, '	Analys	sis ot Ele	ectric Mach	ninery	and D	rive Sys	tems",
	ress, Wiley Inter									
		and Jorg-Andreas Dittrich		or Cont	rol of T	nree- phas	e AC	wachir	nes", Sp	ringer-
Verlag	Berlin and Heid	elberg GmbH & Co. KG, 20	UU8.							

Department : El	Department : Electrical and Electronics Engineering		Programme: M.Tech. (Electrical Drives and Conti			ntrol)		
Semester : Ty	wo	Category : LB						
Cubicat Code	Cubicat	Hou	irs / W	eek	Credit	Maximum Marks		
Subject Code	ode Subject		Т	Р	С	СА	SE	TM
EEE157	Electrical Drives Laboratory	-	-	3	2	60	40	100
Prerequisite				-			<u>.</u>	-
Objectives	To provide a practical understanding of electrical Drives.	of some o	f the co	oncepts	learnt in	the theo	ory coui	se on
Outcome		The Students would have gained practical experience about some of the Theoretica concepts learnt in the electrical Drives.						retical

List of Experiments:

Simulation study of following drive systems

- 1. Analysis of 1 phase angle controlled converter drives and case study on closed loop speed control.
- 2. Analysis of 3 phase angle controlled converter drives and case study on closed loop speed control.
- 3. Transfer function modelling of DC motor and design of speed and current loop feedback controllers.
- 4. Analysis of DC chopper drives and case study on closed loop speed control.
- 5. v/f control of PWM inverter based three phase Induction motor.
- 6. V/f control of three phased Induction motor using Cyclo-converters
- 7. Rotor resistance scheme in wound-rotor Induction motor.
- 8. Slip Power recovery scheme in Wound rotor Induction Machine.
- 9. Modeling of three Phase Square cage Induction Motor
- 10. Direct vector control of Induction Motor.
- 11. Modeling and control of Permanent Magnet Brushless DC motor.

*Demonstration Experiments

- 1. Study of single phase Half controlled converter fed DC motor.
- 2. Study of three phased Inverter fed induction motor drives

Department :	Electrical and Electronics Engineering	Program	nme: N	И.Tech.	(Electrical	Drives a	nd Con	trol)
Semester :	Гwo	Categor	y : P	۲R				
Cubicateda	Cubicat	Hours/v	veek		Credit	Maxin	num m	arks
Subject code	Subject	L	Т	Р	С	CA	SE	ТМ
EE158	Research Methodology	-	-	3	1	100	0	100
Prerequisite	-							
Objectives	To educate students to methodTo expose students to differen			arch pro	blems			
Outcomes	 Students will be capable to ide the requirements of industrial Students will exhibit the dom research effectively Students will possess knowl program. 	and global requ nain skill to cho	ireme bose s	nts suitable	research	methods	to ex	ecute
Theory Philoso • Charae valid, v • Types quanti • Reseau Prepar • Consic of data • Outco Oral p	hesis, Law, Principle. Definition and Dim y: General Theory and Particular/ Empi pophy and validity of research. Objective of cteristics of research: Various functions verifiable, empirical and critical approach of research: Pure and applied resear tative approaches. rch procedure: Formulating the Resear ring the research design including sample lerations in selecting research problem: a, Generalization and interpretation of an me of research: Significance of report w resentation – Mechanics of writing research	rical Theory. Ca of research. that describe cl n. ch. Descriptive cch Problem, Lif e. Design, Samp : Relevance, int nalysis. vriting – Layout search report –	ases a haract and teratu le size erest, s of th	nd their eristics o explanat re Revie available ne reseau	· Limitation of research cory resea ew, Develo e data, cho rch report	ns; Caus n such as rch. Qua pping the pice of d – Types	al Rela s syster alitative e objec ata, An of rep	tions. matic, e and ctives, nalysis orts –
Total contact	rism and copy right violation – Patent wr hours: - Total tutorials: -	Total practical	classe	s:15	То	tal hours	5: 15	
Reference boo		•						
2. Kothar 3. Kumar	n, Catherine, Practical Research Method i, C.R., Research Methodology-Methods , Ranjit, Research Methodology, A Step- ore, 2005.	and Technique	s, Wile	ey Eastei	rn Limited,	New De	lhi, 198	

Department : El	ectrical and Electronics Engineering	Programme: M.Tech. (Electrical Drives and Control)								
Semester : T	hree	Category : PR								
Subject Code	Cubicat	Hou	Hours / Week			Maximum Marks				
Subject Code	Subject	L	L T P			СА	SE	ТМ		
EE159	Project Phase-I	roject Phase-I 9 150 150 30								
Prerequisite		•								
Objectives	To develop a solution to existing problem	m in Eleo	ctric Dr	ives an	d Control					
Outcome	The students can able to perform literat	ure surv	vey, ind	entify	a problem	and solv	/e it			
Description: Ar	n individual project needs to be performed	dividual project needs to be performed by each student under a supervisor. Specific research								
problem needs	to be identified through detailed literature	survey.	The th	eoreti	cal/simulat	tion stud	dy need	s to be		
carried out. The	results along with the literature survey have	e be sub	mitted	as a re	port.					

Department : El	ectrical and Electronics Engineering	Prog	gramme	e: M.Te	ch. (Electri	cal Drive	s and Co	ontrol)	
Semester : F	our	Category : PR							
Subject Code	Subject	Hours / Week			Credit	Maximum Marks			
Subject Code	Subject	L	Т	Р	С	СА	SE	ТМ	
EE160	Project Phase-II	-	-	-	14	200	200	400	
Prerequisite									
Objectives	 To develop an additional solution Phase -I 	• To develop an additional solution or further improvement to the problem identified in Phase -I							
Outcome	 The students can able to compare Can provide improvements, perfor proto-type models 								
•	he problem identified in the Phase-I may be prototype system may be developed with			•	•			•	

SYLLABUS (Elective Subjects)

Department :	Electrical and Electronics Engineering	Progra	mme: l	M.Tech	(Electrical Dr	ives and	Control)
Semester :		Catego	ory :⊺	ΓY				
Subject Code	Subject	Hou	irs / We	eek	Credit	Max	imum N	larks
Subject Code	Subject	L	Т	Ρ	С	СА	SE	ТМ
EEE51	Adaptive Control Theory	3	1	0	4	40	60	100
Prerequisite								
Objectives	 To understand the basics of ada To understand the concept of m To know the adaptation mechan To know few applications of ada 	nodel ref nisms su	erence ch as a	•	•	chedulin	g	
Outcome:	 Understands the model referent Know the auto-tuning and gain Understands the applications or 	scheduli	ng met	hods	ents to nhysic	al systen	ıs	
UNIT – I	Modeling and Simulation	laaptiv	e conti		Hours: 9	ai systeri	15	
	ck. Effect of process variations. Classifica	tions of	Adapti	ve cont		, Freque	ency , In	npulse,
	methods. Simulations of 1 st and 2nd orde		-			, ,	, ,	. ,
UNIT – II	Identification Technique				Hours: 9			
Off-line. on lin	e methods . Least square. Recursive leas	t square	, fixed	memor	y .maximum l	ikelihood	l. Instru	mental
variable. Stoch	astic approximate method.							
UNIT – III	MRAS & STC				Hours: 9			
	the gradient approach . MIT rule Liap ance control. Predictive control.	unov Fu	nctions	. Contr	olpolicies . Po	ole place	ement c	ontrol.
UNIT – IV	Auto-Tuning and Gain Scheduling				Hours: 9			
ID control . a	uto tuning technique . Transient respo	onse me	thods.	Metho	ds based on	relay fe	edback.	Relay
	rinciple and design of gain scheduling co	ontroller	s. Non	linear t	ransformation	ns. Appli	cations	of gain
scheduling. UNIT – V	Applications and Expert Control				Hours: 9			
	otive controllers. Process control. ship	steering	. Ada	otive si		ng. Fxtre	emum c	ontrol.
	system . Learning systems. Introduction 1	-						0
Total contact I			-	l Classe		tal Hour	s:60	
Text Books:					•			
1. Karl.J.A	Astrom, Bjorn Wittenmark, Adaptive Cont	trol, Pea	rson Ed	ucation	, pvt. Ltd 199	5.		
2. Goodw	vin G.C Sin KS New Jersey, Adaptive Filter	ing, Prec	liction a	and con	trol, Prentice	Hall inc.	1984.	
Reference Boo	lks:							
1. Harris	C.J. Billings. S.A., Self tuning and Adaptive	e control	, Peter	peregir	nius Ltd., 1984	l.		
2. Iserma	nn R, Digital Control System vol. I & II, Na	arosa Pul	blishing	, House	, Reprint 1993			
3. Menda	al IM Marcel dekkas. Discrete Technique	of Parar	neter F	stimate	New York 1	973.		

Department :	Electrical and Electronics Engineering	Progra	mme:	M.Tech	. (Electrical Dr	ives and	Control)
Semester :		Catego	ory : ⁻	ΓY				
Subject Code	Subject	Ηοι	irs / W	eek	Credit	Max	imum N	/larks
Subject Code	Subject	L	Т	Р	С	CA	SE	ТМ
EEE52	Advanced Digital Signal Processing	3	1	0	4	40	60	100
Prerequisite								
	To understand the concepts of	digital si	gnals a	nd syste	ems			
	 To understand the design of dig 	gital filte	rs					
Objectives	 To know the concept of adaptive 	e filters	and to	know it	s applications			
	 To understand the concept of s 	ampling	rate alt	teration				
	 To study and understand the di 	fferent D	Digital s	ignal pr	ocessors.			
	 Analyze and classify the digital 	signals a	nd syst	ems.				
Outcome	 Design digital filters, adaptive f 	ilters						
Outcome	 Do sampling rate alterations 							
	 Understand and choose differe 	nt digital	signal	process	ors for electri	cal contr	ol appli	cations
UNIT – I	Discrete Time Signals				Hours: 9			
	o Discrete time signals LTI system-stabilit					-		ation of
	ignals and systems .Discrete random sigr	als. Z-tra	ansforn	ns. prop		e Z transf	orms.	
UNIT – II	Digital Filter Design				Hours: 9			
	lter. filter structures . Design from analog	g filter; D	Design o	of FIR fi	lters - structu	res. wind	owing -	Design
examples.								
UNIT – III	Adaptive Digital Filters				Hours: 9			
	s. Examples of Adaptive filtering. The m		mean s	quare e	error criterion	; The Wi	drow ar	nd Hoff
-	. Recursive least square Algorithm .Appli							
	Application of Sampling Rate Alteratio				Hours: 9			
	nple rate Alteration Devices-Filters with	•	-		•		-	-
design -Applica	d Interpolators. Arbitrating rate sampli	ng rate	conven	ler. Poi	y-phase deco	προδιτιο	n. uigita	armer
UNIT – V	Digital Signal Processors				Hours: 9			
	processors. Introduction DSP processor	memory	. Archit	octuro			P proce	score
	verview of TMS 320 family DSP process						•	
320cbx proces		01 .11150	genere			J JIXTI B		
Total contact I		Total F	Practica	l Classe	es: To	tal Hour	s:60	
Text Books:								
	K. Mitra, Digital signal processing: A Co	mputer	Based	Approa	ch, Tata McG	raw hill	Pub. Co	mpany
-	d New Delhi, 2001.	•		••	,			. ,
2. Andrea	as Antoniou, Digital Filters: Analysis, D	Design a	nd App	olicatior	n. Tata McGr	aw hill I	Pub. Co	mpany
Limite	d New Delhi, 2001.							
Reference Boo	oks:							
1. Alan C	ppenheim. V and Ronals W. Schafer, Dig	ital Signa	al proce	essing,	Prentice Hall of	of India P	rivate L	imited,
	velhi, 1992.							
	Haylaim and Barry van veen, Signals and	-		-				
3. S.Saliv	ahanan, Digital signal processing, Tata M	c Graw H	ill Edu	cation P	rivate Limited	l, New De	elhi, 201	.0.

	Electrical and Electronics Engineering				. (Electrical Dr	ives and	Contro)
Semester :		Catego		TY	Cuedit			A a vilva
Subject Code	Subject	HOU	urs / W T	еек Р	Credit C	CA	kimum N SE	Viarks TM
EEE53	Diagnosis and Protection for Solid State Systems	3	1	Р 0	4	40	60	100
Prerequisite								
	 To understand the basics of adaptive standard stand standard standard stand standard standard stan standard	antive co	ntrol					
	 To understand the basies of date To understand the concept of r 	•		adapti	ve systems			
Objectives	 To know the adaptation mecha 			•	•	chedulir	וס	
	 To know few applications of ad 					licuan	'ð	
	 Understands the model referer 			stems				
Outcome	 Know the auto-tuning and gain 	•	•					
	 Understands the applications of 		-		epts to physic	cal system	ms	
	Protection and Fault Diagnosis of Conv		00110				Hours: 9	9
UNIT – I	Systems							
Protections to	SCR based power conversion systems: d	evices, co	onverte	ers. nati	urally commut	tated co	nverters	. sing
	se converters . dual converters . cyclo-	-			•			•
	rters. Fault diagnosis of converters: devi							
failures; Fault	diagnosis of control loops: failure of c	ontrolle	r and I	imiters,	sensor and	referenc	e, starti	ing an
braking.								•
UNIT – II	Protection and Diagnosis of Solid State	e Devices	s in			1	Hours: 9)
	Power Systems							
Protections to	solid state compensators/voltage regu	lator. TC	R, TCS	, SVC, 1	TCSC, UPFC, s	olid stat	te tap cl	hange
Fault diagnosi	s through waveform/performance and	alysis of	devic	e failur	es, phase fa	ilures, s	ensor f	ailure
Protection and	fault diagnosis of filter. aging of passive	compon	ents ar	nd detu	ning. auto tun	ing		
methods.								
	Protection and Fault Diagnosis of Solic						Hours: 9	
Protections to	solid state DC drives. field failures, ar	mature	failures	s, comn		/open, c	peratio	ns wit
Protections to converter/cho	solid state DC drives. field failures, ar pper failures . device, input source, filter	mature r compoi	failures nent fai	s, comn ilures. (Closed loop co	/open, c ontrol fai	peration lures. fa	ns wit ilure d
Protections to converter/cho controllers and	solid state DC drives. field failures, ar	mature r compoi	failures nent fai	s, comn ilures. (Closed loop co	/open, c ontrol fai	peration lures. fa	ns wit iilure d
Protections to converter/cho controllers and braking.	solid state DC drives. field failures, ar pper failures . device, input source, filter d limiters, sensor and references. Diag	mature compoinosis of	failures nent fai solid s	s, comn ilures. (Closed loop co	/open, c ontrol fai ns faults	peration lures. fa - starti	ns wit iilure c ing an
Protections to converter/cho controllers and braking. UNIT – IV	solid state DC drives. field failures, ar pper failures . device, input source, filter d limiters, sensor and references. Diag Protection And Diagnosis Of Solid Stat	mature compoinosis of e AC Driv	failures nent fai solid s ves	s, comn ilures. (state do	Closed loop co drive systen	/open, control fai	peration lures. fa 5 - starti Hours: 9	ns wit iilure d ing an
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Press . A John Wiley and Sons, Inc. Publications. Year.

- 3. Bin Wu, High-Power Converters and AC Drives, Wiley-IEEE Press.
- 4. Vijay K. Sood, HVDC and FACTS Controller: Application of Static Converters in power systems, IEEE Power Electronics and Power Systems series, Kluwer Academic publishers, Boston, 2004.
- 5. Vector Control of Three-Phase AC Machines: System Development in the Practice, Nguyen Phung Quang, Jörg-Andreas Dittrich, Springer, 2008.

-	Electrical and Electronics Engineering	_			. (Electrical	Drives and	Contro	·/
Semester :		Catego	ory :	ΓY				
Subject Code	Subject	Ηοι	urs / W	eek	Credit	Max	imum I	Marks
Subject code		L	Т	Р	С	CA	SE	TM
EEE54	Embedded Systems	3	1	0	4	40	60	100
Prerequisite								
	 To know the concept of embed 							
	 To understand the architecture 	ofembe	edded s	ystem				
Objectives	 To know the peripherals used i 	n embed	lded sys	stem				
	 To know the concept of RTOS 							
	 To know the Testing and valida 	tion of e	mbedd	ed syste	em			
	 Understands the architecture of the second se	of embed	ded sys	stem				
Outcome	 Knows the peripherals used in 	embedde	ed syste	em				
outcome	Understands the performance	metrics o	of RTOS					
	 Knows the testing and validation 	on of eml	bedded	system	IS			
UNIT – I	Introduction to Embedded Systems				Hours: 9			
Introduction to	embedded system -Definition and Class	sification	– Over	view of	f Processors	and hardv	vare un	its in a
embedded sys	tem – Software embedded into the sys	tem – Ex	emplar	y Embe	edded Syste	ems – Emb	edded S	System
on a Chip (SoC)					1			
UNIT – II	Embedded System Architecture				Hours: 9			
Microcontrolle	r Architecture - Motorola 68HC11- PIC-	Memor	y Syste	m Arch	nitecture -C	aches - Vir	tual M	emory
•	gement Unit and Address Translation - I			•			•	
Co- processors	and Hardware Accelerators - Proces							
	s and hardware Accelerators - Froces	sor Perr	ormand	e -Enh	ancement	- Pipelinin	g -Supe	er-scala
Execution.		sor Perf	ormand	e -Enh		- Pipelinin	g -Supe	er-scala
Execution. UNIT – III	Embedded Computing Platform				Hours: 9	•		
Execution. UNIT – III CPU Bus - Bus	Embedded Computing Platform Protocols - Bus Organization - Memo	ry Device	es and	their C	Hours: 9	cs - RAM	ROM, L	JVROM
Execution. UNIT – III CPU Bus - Bus EEPROM, Flash	Embedded Computing Platform Protocols - Bus Organization - Memor Memory - DRAM - I/O Devices - Timer	ry Device	es and	their C	Hours: 9	cs - RAM	ROM, L	JVRON
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Department :	Electrical and Electronics Engineer	ing	Program	nme: M. ⁻	Tech. (E	lectrical D	rives and	d Cont	rol)
Semester :			Catego	•			F		
Subject Code	Subject		Но	urs / We	ek	Credit	Maxim	num M	larks
Subject code	Subject		L	Т	Р	С	СА	SE	ТМ
EEE55	Flexible AC Transmission System		3	1	0	4	40	60	100
	Controllers		Ĵ	-	Ũ				
Prerequisite		-							
	• To emphasize the need f					-			
Objectives	• To learn the character	ristics, app	olications	and mo	deling	of series	and sh	unt F	ACTS
	controllers.	c 1:cc				c			
	To analyze the interactio				-				
Outcome	 Ability to design and dev Able to identify antimum 	•			•		ansmissi	on sys	tems
	Able to identify optimum	I FACTS CO	ntroller for	specific					
UNIT – I	Introduction	orke contre		or flow i	Hours:		n line	Analy	ic of
	ics of power transmission netwo d AC Transmission line- Passiv								
	at the mid-point of the line of		•	•					
controllers.	at the find point of the fine of				TACIS	controlle	is type.	5 01 1	
UNIT – II	Static VAR Compensator (SVC)				Hours:	9			
-	uration of SVC- voltage regulation	by SVC- M	odelling of	SVC for	load flo	w analysis	- Model	ling of	SVC
-	dies-Design of SVC to regulate the	•	-					-	
	cement and power oscillation dan	•	-						the
line.			-				-		
UNIT – III	THYRISTOR AND GTO THYRIS	STOR CON	TROLLED	SERIES	Hours:	9			
	CAPACITORS (TCSC and GCSC)								
	ontrolled Series Compensation – C								
	for load flow studies modeling TC			oility stuc			of TCSC	and G	CSC.
UNIT – IV	Voltage Source Converter Based				Hours:	-			
	nous compensator (STATCOM)-				-				
	SSSC-Power flow control with STA			•					
	stability studies –operation of					controllei	S(UPFC	and I	PFC)-
-	PFC and IPFC for load flow and tra					0			
	Controllers and Their Coordinati Controller interactions – SVC–SVC				Hours:		orcusing	linoa	r
	ues – Quantitative treatment of c				munipi	econtion	ers using	sinea	1
Total contact I			otal Practic	al Classe	د.	Total	Hours: 6	0	
Text Books:					5.	Total	10415.0	0	
	n, "Flexible AC Transmission Syste	em". Institu	ution of Fle	ectrical ar	nd Electi	ronic Engi	neers (IF	FF), 1	999.
	G.Hingorani, Laszio. Gyugyl, "U					-	-		
	hission System", Standard Publishe		-				07		
	od, "HVDC and FACTS controllers-			c Conver	ters in F	ower Sys	tem", 20)04 <i>,</i> КІ	uwer
Acade	nic Publishers.								
Reference Boo	ks:								
1. Mohar	Mathur, R., Rajiv. K. Varma, "	"Thyristor	– Based F	acts Co	ntrollers	for Elec	trical Tr	ansmi	ssion
	s", IEEE press and John Wiley & So								
	diyar," FACTS Controllers in Powe	er Transmis	ssion and	Distribut	ion", Ne	ew Age Ir	nternatio	nal(P)	Ltd.,
Publis	ers New Delhi, Reprint 2008.								

Department :	Electrical and El	ectronics Engineering	Progra	mme:	M.Tech	. (Electrica	l Drives and	Contro)
Semester :	_		Catego	ory : ⁻	ΤY				
Subject Code	Subject		Ηοι	ırs / W	eek	Credit	Max	imum N	Лarks
Subject Code	Subject		L	Т	Р	С	CA	SE	ТМ
EEE56	FPGA Based S	ystem Design	3	1	0	4	40	60	100
Prerequisite									
Objectives	To urTo kn	ow various programmabl Iderstand the FPGA based Iow the FPGA based comb Iow the FPGA based large	l system pinationa	design al and s		ial logic de	signs		
Outcome	UnderAbility	stands various PLDs and i to design FPGA based sy to design FPGA based co	ts uses stem		d seque	ential logic	systems		
UNIT – I	Introduction					Hours: 9			
FPGAs, Chip I/	-	Types-PLA, PAL, FPGA-arc o of FPGA Fabrics. Archite				ed FPGAs,	Permanentl	y Progr	ammed
UNIT – II	FPGA-Based S	ystems and VLSI Technol	ogy			Hours: 9			
Introduction, I	Basic Concepts,	Digital Design and FPG	As. FPG	A-based	d systei	m design.	Manufactur	ring Pro	ocesses,
Transistor Cha	racteristics, CM	OS Logic Gates, Wires, Re	gisters a	nd RAN	Л, Packa	ages and P	ads.		
UNIT – III	Combinationa	l Logic				Hours: 9			
-	-	rdware Description Langu c, logic implementation	-				•		
UNIT – IV	Sequentional	Machines				Hours: 9			
The sequential Optimization.	machine desig	n process. Sequential des	ign style	es. Rule	s for Cl	ocking. Pe	rformance A	Analysis	. Power
UNIT – V	Large Scale Sy	stems				Hours: 9			
	-	le Systems, Behavioral ystems, Novel Architectur		Design	Metho	odologies.	Design Exa	ample.	Busses,
Total contact I	Hours: 45	Total Tutorials: 15	Total F	Practica	l Classe	es:	Total Hour	ˈs:60	
Text Books:									
-		ased System Design", Pre			ŀ.				
		n VLSI Design", Pearson Ec	ducation	2002.					
REFERENCE BC									
		vanced Digital Design with	-	HDL", I	Pearsor	educatior	n 2005		
		nitkar, Pearson Education		-					
	-	IDL Primer", B S Publicatio			n 2004	1			
4. Kevin S	skanili, VHDLT(or Programmable Logic, P	earson E	uucatio	יות, 200 ²	+.			

Department :	Electrical and Electronics Engineering	Progra	mme:	M.Tech.	. (Electrical	Drives and	Control)
Semester :		Catego	ory : ⁻	ΓY				
Subject Code	Subject	Ηοι	irs / W	eek	Credit	Max	imum N	/larks
Subject code	Subject	L	Т	Р	С	CA	SE	ТМ
EEE57	Fuzzy Control	3	1	0	4	40	60	100
Prerequisite								
Objectives	 To understand fuzzification and To know the design of fuzzy logi To study few design examples o To understand the applications system To know the applications of fuzz 	c contro f fuzzy c s of fuzz	l ontrol y logic	to nor		-		tion of
Outcome	 Knows the concept of fuzzy logic Able to design fuzzy logic contro Able to use fuzzy logic for nonlin Know the application of fuzzy logic 	c, fuzzifi ollers near ana	cation a	and def	uzzificatior ifications			
UNIT – I	Introduction				Hours: 9			
Crisp Sets, Fuz	zzy Sets, Linguistic Variables, Values and	d Rules,	Rule	Base, F	uzzificatior	, Members	ship Fur	nctions-
Types, Inference	ce Mechanism, Defuzzification, Takagi-Su	geno Fu	zzy Syst	tems.				
UNIT – II	Fuzzy Control				Hours: 9			
Conventional (Control System- Design. Fuzzy control sy	/stem- o	choice	of cont	roller inpu	ts and out	outs, ru	le base
using control k	nowledge, fuzzy quantification of knowle	dge, rul	e deter	minatio	on, converti	ng decision	s into a	ctions.
UNIT – III	Design Examples of Fuzzy Control				Hours: 9			
The inverted		•		ns-basic	•	guidelines,	real	time
	n issues-computational time, memory r			ypical d	-	nple.		
UNIT – IV	Nonlinear Analysis, Identification and E				Hours: 9			
	fuzzy controllers, fuzzy-P, PI, PID contro							
	estimation- fitting functions to data, leas	st squar	es met	hod, gra	adient met	hods, clust	ering m	ethods
	of rules from data.							
UNIT – V	Adaptive and Supervisory Fuzzy Contro				Hours: 9			
knowledge bas scheduling, sup	reference learning control (FMRLC)-re sed modifiers, design and implementatio pervision of fuzzy control-rule based supe	n, cases rvision-	tudy. S case stu	upervis udy.	ion of fuzz		-	
Total contact H	Hours: 45 Total Tutorials: 15	Total F	Practica	l Classe	es:	Total Hou	rs:60	
	Control, Kevin M. Passino and Stephen Yu ogic with Engineering Applications, Timot				-	0.		
Reference Boo								
2. R.K. Ya 3. Klir G.J	Wang, A Course in Fuzzy Systems and Con ger, D.P.Filev, Essentials of Fuzzy Modelir and B.O.Yuan, Fuzzy Sets and Fuzzy Logic r Driakov et al, An Introduction to Fuzzy C	ng and C : Theor	ontrol, y and A	John W pplicati	/iley Sons i ons, PHI, Ir	ndia, 1997.	k, 1994	

Semester :	Electrical and El	ectronics Engineering				n. (Electrica	al Drives	s and Co	ntrol)
Jemester .			Cate		TY				
Subject Code	Subject		Ho	ours / V	/eek P	Credit C	Max CA	imum N SE	/larks TM
EEE58	Modern Powe	er Electronic Converters	3	1	0	4	40	60	100
Prerequisite								1	
Objectives		derstand the design of SN derstand the operation o		ned and	soft sw	vitched con	verters		
Outcome	Stude	nts can able to design SM nts can able to analyse re	IPS specific t	o appli	cation.				
UNIT – I		de Power Supplies (SMPS		,	1	ours: 9			
multiple outp negative and	uts; Pump circu double output;	fication; Switched mode hits - developed, transfor SEPIC converter. Voltage control and regulation; D	mer type a e-lift conver	nd sup ters an	er lift p d Super	umps; Luo [.] lift conve	conver rters -t	ters - p ypes an	ositive d basi
UNIT – II	AC-DC Conver	rters			Но	ours: 9			
Switched mod	le ac-dc conve	rters – synchronous rec	tification -	single a	and thre	ee phase t	topolog	ies - sv	vitchin
without input	output isolatio	r factor – reduced input c n – performance indices lundancy, reliability.				•		•	
UNIT – III	DC-AC Conver	· · ·			Но	ours: 9			
and harmonic balancing. Z - S UNIT – IV Matrix conver scalar modula operation, ma operation - w	content; Comp Source converte AC-AC Conver ters – Basic top tion, indirect m in features an	lying capacitor and casca parison of topologies – d ers; Active filters – topolog ters with and without Do pology of matrix convert iodulation; Matrix convert d analysis, types and ap t resonance link - conve converters.	levice stress gies, operati <mark>C Link</mark> er; Commut rter as only plications A	, losses on and ation - ac-dc c C-AC c	s, comp closed l Ho curren converte	onent cour oop contro ours: 9 t path; Mo r; Vienna I r with DC	nt and ol. odulatio Rectifier link - 1	dc link on techr r – Prin topolog	voltag niques ciple c ies and
UNIT – V	1	g Power Converters			Но	ours: 9			
Power electro switching tech switched conv	nic converters niques – ZVS, Z verters –ac-dc d	 analysis and determined CS, ZVT, quasi resonance converter, dc-dc converted supplies; Introduction to 	operation; F er, dc-ac co	erform nverter	osses – ance co – ac-ad	loss reduce mparison h c converte	nard sw	itched a	and sof
Total contact		Total Tutorials: 15	Total Prac			-	al Hour	rs:60	
Text Books: 1. Powe 2. Advar 3. Contro	r Electronics Ha nced DC/DC Cor ol in Power Elect nnan and Frede	ndbook, M.H.Rashid, Aca werters, Fang Lin Luo and tronics- Selected Problem Blaabjerg, Academic Pres	demic press Fang Lin Lu , Marian P.K	, Newyo o, CRC azmier	ork, 200 Press, N kowski,	0.			
- (=									
Reference Boo	atarseh Power I	Electronic Circuits, John W	Viley and So	ns, Inc.2	2004.				

	lectrical and El	ectronics Engineering				. (Electrical D	rives and	Contro)
Semester :			Catego	-					
Subject Code	Subject		Но	ırs / W	eek	Credit	Max	imum N	/larks
Subject code	Subject		L	Т	Р	С	CA	SE	TM
EEE59	Neural Netwo	orks	3	1	0	4	40	60	100
Prerequisite									
Objectives	 archit The call and estimate In the literat MATL 	paper exposes the stud ecture, algorithms and de apability and applicability stimation would be cover a last unit the major a ure is discussed. AB based programming e nowledge and skill to bu	esign. y of Neu ed. oplicatio xercises	ral Net ns of I and Sin	work b NN to nulink t	based solution power electr boolbox will be	s for mo ronics an used.	deling, d drive	contro s fron
Outcome	• The st learni	z udent would acquire the ng algorithm to design NI ment the same for proble	l based s	olutior	ns.		Neural ar	chitectu	ure and
UNIT – I	Introduction					Hours: 9			
Introduction –	Biological neur	al network – Artificial N	eural net	work –	- compa	arison, motiva	ation and	Develo	pmen
Neuron model	– single / mu	Iltiple inputs, transfer fu	inctions.	Netwo	ork arc	hitecture – si	ngle /mu	ultiple l	ayers
Recurrent netw	vorks Perceptro	n network – architecture	, learnin	g rule, l	linear s	everability lim	itation.		
UNIT – II	Supervised Le	earning				Hours: 9			
Learning mech	ianism – supe	rvised learning – multip	layer pe	erceptro	ons for	pattern clas	sificatior	and f	unctio
approximation	. The back pr	opagation algorithm -	numerio	al exa	mples.	Drawbacks i	n Back	propaga	ation
Momentum m	ethod, variable	learning rate, Levenburg	Margu	ardt Al	gorithm	n. Other super	vised lea	rning m	ethod
 supervised H 	ebbs rule, Widr	ow Hoff learning rule – A	daline ne	etwork.					
UNIT – III	Associative No	etworks				Hours: 9			
Associative lea	rning – unsup	ervised Hebb's rule – In	star leai	ning ru	ule – K	ohonen rule,	Outstar	rule –	Patter
association –	Hetero associa	tive, Auto associative a	nd Bi-di	rection	al asso	ciative memo	ory – Dis	crete F	lopfiel
network – Arch	itecture, algori	thm.							
UNIT – IV	Competitive &	& Self Organizing Networ	ks			Hours: 9			
		ed weight competitive							
		r quantisation – archit	ecture,	algorit	hm. Ao	daptive resor	nance th	eory –	ART
architecture, al	gorithm.								
UNIT – V	Applications t	o Electrical Drives and Co	ontrol			Hours: 9			
		odulator, Estimation- Mo	•			-	using Ne	ural Ne	twork
Choice of Neur	al architectures	and training algorithms	for the v	arious a	applicat	ions.			
Total contact H	lours: 45	Total Tutorials: 15	Total I	Practica	al Classe	es: To	otal Hour	rs:60	
Text Books:									
2. Lauren Educat	e Faseff, Fund ion 2004.	vard B.Demuth and Mark damentals of Neural No n Power Electronics and	etworks-	archite	cture,	algorithm an	d applica	ation, F	Pearso
Reference Boo	ks:								
 James technic Robert Sathis 	A.Freemen an ques , Addison V J.Schalfoll, Arti kumar, Neural I	d David M.Skapura, Ne Nesley Publishing House ficial Neural Network, Ta Network , Tata McGraw H Networks: A Compreher	1992 ta McGra Iill 2004	aw Hill	Co, 199	7.		progra	immin

Semester :					(Electrical Dr			
		Catego	2	TY		1		
Subject Code	Subject		irs / W		Credit		imum N	1
-		L	T	P	C	CA	SE	TM
EEE60	Nonlinear Control Systems	3	1	0	4	40	60	100
Prerequisite	-							
Objectives	 To understand the different no To analyze the stability analysis To know the different methods the analysis of nonlinear system 	of non-l of linea	inear s rization	ystem u of nonl	sing Lyapuno inear system	s		s
Outcome	 Identify different nonlinearities Analyze the stability of nonline Linearize nonlinear systems Analyze nonlinear systems usin 	near systems						ods.
UNIT – I	Properties of Nonlinear Systems				Hours: 9			
Basic mathem	atical and structural models of nonlinear	r system	s – basi	ic prope	erties of nonli	inear syst	tems - S	tabilit
and Equilibriu classes.	m States – basic properties of nonlinear	function	s - Typ	ical Nor	nlinear Eleme	nts – bas	sic nonli	nearit
UNIT – II	Stability				Hours: 9			
Equilibrium St	ates and Concepts of Stability - Stability	of a No	nlinear	System	Based on Sta	abilitv of	the Lin	earize
UNIT – III Graphical Line Linearization (lity of Forced Nonlinear Systems. Linearization Methods earization Methods - Algebraic Lineari	zation -	Analyt	ical Lin	Hours: 9	lethod -	Evalua	tion c
I Inearization -	Coefficients by Least-Squares Method -							
	Combined (Dual-Input) Describing Funct				n - Describir			
UNIT – IV	Combined (Dual-Input) Describing Funct Phase Trajectories	ions.	nic Line	arizatio	n - Describir Hours: 9	ng Functi	on - Sta	atistica
UNIT – IV Operating Mod Signal-Dither -	Combined (Dual-Input) Describing Funct Phase Trajectories des of Nonlinear Control Systems - Self-C Methods of Dynamic Analysis of Nonline stems - Methods of Defining Phase Traj	ions. Oscillatio ear Syste	nic Line ns – Fo ms - Pł	rced Os	n - Describir Hours: 9 cillations - Ef ne - Phase Tr	fects of H fajectorie	on - Sta ligh-Fre	atistica quenc ear and
UNIT – IV Operating Mod Signal-Dither - Non linear Sys	Combined (Dual-Input) Describing Funct Phase Trajectories des of Nonlinear Control Systems - Self-C Methods of Dynamic Analysis of Nonline stems - Methods of Defining Phase Traj	ions. Dscillatio ear Syste jectories	nic Line ns – Fo ms - Pł - Exan	rced Os	n - Describir Hours: 9 cillations - Ef ne - Phase Tr	fects of H fajectorie	on - Sta ligh-Fre	atistica quenc ear and
UNIT – IV Operating Mod Signal-Dither - Non linear Sys obtain Phase T UNIT – V Harmonic Line Describing Fur Oscillations -D Nonlinear Syst in Tracking Mod	Combined (Dual-Input) Describing Funct Phase Trajectories des of Nonlinear Control Systems - Self-O Methods of Dynamic Analysis of Nonline stems - Methods of Defining Phase Traj rajectories Dynamic Analysis of Non-Linear Contro earization in Dynamic Analysis of Nonl nction in Dynamic Analysis of Unforced etermination of Symmetrical Self-Oscilla tems - Resonance Jump – Harmonic Line de of Operation	ions. Dscillatio ear Syste jectories ol Systen inear Co Nonline tions - A arizatior	nic Line ns – Fo ms - Ph - Exan ntrol S ar Cont symme i in Dyr	rced Os nase Pla nples of Systems crol Systems trical So namic A	n - Describin Hours: 9 cillations - Ef ne - Phase Tr Application Hours: 9 Operating i tems - Analysis elf-Oscillation nalysis of No	fects of F ajectorie of Variou n Stabili: sis of Syr us-Forced nlinear C	on - Sta ligh-Fre s of Line us Meth zation 1 mmetric Oscillat ontrol S	quence ear an nods t Mode al Seli cions c ystem
UNIT – IV Operating Moo Signal-Dither - Non linear Sys obtain Phase T UNIT – V Harmonic Line Describing Fur Oscillations -D Nonlinear Syst in Tracking Moo Total conta	Combined (Dual-Input) Describing Funct Phase Trajectories des of Nonlinear Control Systems - Self-O Methods of Dynamic Analysis of Nonline stems - Methods of Defining Phase Traj rajectories Dynamic Analysis of Non-Linear Contro earization in Dynamic Analysis of Nonline nction in Dynamic Analysis of Unforced etermination of Symmetrical Self-Oscilla rems - Resonance Jump – Harmonic Line de of Operation	ions. Dscillatio ear Syste jectories ol Systen inear Co Nonline tions - A arizatior	nic Line ns – Fo ms - Ph - Exan ntrol S ar Cont symme i in Dyr	rced Os nase Pla nples of Systems trol Systems	n - Describin Hours: 9 cillations - Ef ne - Phase Tr Application Hours: 9 Operating i tems - Analysis elf-Oscillation nalysis of No	fects of F ajectorie of Variou n Stabili: sis of Syr us-Forced nlinear C	on - Sta ligh-Fre s of Line us Meth zation I mmetric Oscillat	quence ear an nods t Mode al Sel cions c ystem
UNIT – IV Operating Mod Signal-Dither - Non linear Sys obtain Phase T UNIT – V Harmonic Line Describing Fur Oscillations -D Nonlinear Syst in Tracking Mo Total contae Text Books: 1. Shanka	Combined (Dual-Input) Describing Funct Phase Trajectories des of Nonlinear Control Systems - Self-O Methods of Dynamic Analysis of Nonline stems - Methods of Defining Phase Trajectories Dynamic Analysis of Non-Linear Contro earization in Dynamic Analysis of Nonline nction in Dynamic Analysis of Unforced etermination of Symmetrical Self-Oscilla ems - Resonance Jump – Harmonic Line de of Operation ct Hours: 45 Total Tutorials: 15 ar Sastry, Nonlinear Systems Analysis, Stat n K. Khalil, Nonlinear systems, Prentice H	ions. Dscillatio ear Syste jectories ol Systen inear Co Nonline tions - A arizatior Tot	nic Line ns – Fo ms - Pł - Exan ontrol S ar Cont symme i in Dyr tal Prac	rced Os nase Pla nples of Systems trol Sys trical So namic A tical Cla	n - Describir Hours: 9 cillations - Ef ne - Phase Tr Application Hours: 9 Operating i tems - Analysis elf-Oscillation nalysis of Nor asses:	fects of H rajectorie of Variou n Stabili: sis of Syr ns-Forced nlinear C Total	on - Sta ligh-Fre s of Line us Meth zation 1 mmetric Oscillat ontrol S	quence ear an nods t Mode al Sel cions c ystem
UNIT – IV Operating Mod Signal-Dither - Non linear Sys obtain Phase T UNIT – V Harmonic Line Describing Fur Oscillations -D Nonlinear Syst in Tracking Mod Total contac Text Books: 1. Shanka 2. Hassar Reference Boo	Combined (Dual-Input) Describing Funct Phase Trajectories des of Nonlinear Control Systems - Self-O Methods of Dynamic Analysis of Nonline stems - Methods of Defining Phase Trajectories Dynamic Analysis of Non-Linear Contro earization in Dynamic Analysis of Nonline nction in Dynamic Analysis of Unforced etermination of Symmetrical Self-Oscilla ems - Resonance Jump – Harmonic Line de of Operation ct Hours: 45 Total Tutorials: 15 ar Sastry, Nonlinear Systems Analysis, Stat n K. Khalil, Nonlinear systems, Prentice H	ions. Dscillatio ear Syste jectories ol Systen inear Co Nonline tions - A arizatior To ability an all, 2001	nic Line ns – Fo ms - Pł - Exan ontrol S ar Cont symme i in Dyr tal Prac	rced Os nase Pla nples of Systems trol Sys trical So namic A tical Cla	n - Describir Hours: 9 cillations - Ef ne - Phase Tr Application Hours: 9 Operating i tems - Analysis elf-Oscillation nalysis of Nor asses:	fects of H rajectorie of Variou n Stabili: sis of Syr ns-Forced nlinear C Total	on - Sta ligh-Fre s of Line us Meth zation 1 mmetric Oscillat ontrol S	quence ear an nods t Mode al Sel cions o ystem

Department :	Electrical and Electronics Engineering	Progra	mme:	M.Tech	. (Electrica	l Drives a	nd Contr	ol)
Semester :		Catego	ory : ⁻	ΓY				
Subject Code	Subject	Ηοι	rs / W	eek	Credit	N	laximum	Marks
Subject Code	Subject	L	Т	Р	С	C/	A SE	ТМ
EEE61	Optimal Control Theory	3	1	0	4	40	60	100
Prerequisite	Modern Control Theory							
Objectives	 To know the formulation of opt To understand the method of d To know the concept of function To know the pontryagin's princi The numerical methods to solve 	ynamic p nal and o ple and	orogran constra to solve	nming ined op e regula	itor proble	•	S	
Outcome	 Ability to Formulate optimal con Ability to Solve optimal control Ability to Solve minimum time/ Ability to Solve optimal control 							
UNIT – I	Performance Measure				Hours: 9			
	ulation - state variable representation	of syste	ems –p	perform	ance mea	sures for	r optima	l control
	ecting a performance measure.							
UNIT – II	Dynamic Programming ol law – principle of optimality – App				Hours: 9			
	ation of Dynamic Programming – Imbed ms – Discrete Linear regulator Problems lems.	-	-	-	-			-
UNIT – III	Calculus of Variations				Hours: 9			
	concepts – Functional of a single function of a single function of the extremals – constrained extrema.	on – fun	ctional	s involv	ing severa	l indepei	ndent fu	nctions –
UNIT – IV	Variational Approach to Optimal Contr	ol Probl	em		Hours: 9			
	dition for optimal control – Linear regula straints – Minimum time Problems –M I Problem.	•					•	
UNIT – V	Numerical Methods of Optimal Control				Hours: 9			
Simplex Metho	d – golden section Method – Hill climbin	g – Grad	ient – F	Penalty	functions i	methods.		
Total contac	t Hours: 45 Total Tutorials: 15	Tot	al Prac	tical Cla	asses:	То	tal Hour	s:60
Text Books:								
1962.	I.E.Kirk, Optimal Control Theory, an Intro D. O. Anderson and J. B. Moore, Optimal o		-			lewood (Cliffs, Ne	w Jersey,
Reference Boo	ks:							
2. Michae	w P. Sage, Optimum Systems Control, Pre el Athans and Peter L Falb, Optimal contro 5., Optimization Theory and Application, N	ol, Dove	r public	ations,				

-	Electrical and Electronics Engineering				i. (Electrical Dri	ves and	Control)
Semester :		Catego	-	TY				
Subject Code	Subject	Hou L	irs / W T	eek P	Credit C	Max CA	imum N SE	/larks TM
EEE62	Power Electronics in Power Systems	с 3	1	Р 0	4	40	60	100
Prerequisite			_	Ū				
Objectives	 To study about reactive power To study the Applications of co 	•				R compe	nsators	
Outcome	Ability to determine the characeAbility to design different type					•		Ā
UNIT – I	Reactive Power Requirements				Hours: 9			
Power system	components - representation of single l	ine diag	gram –	uncom	pensated lines	- comp	ensator	s types
- principles of phase angle r	stics - conventional compensator -moder reactive power control – introduction on egulation. Compensator requirements for rmonics for various converters – power	load cor or solid	npensa state o	ation - I convert	ine compensat ers – determir	ion – P a nation o	and Q co of input	ontrol - power
UNIT – II	Reactive Power Compensation and Reg	ulation			Hours: 9			
Load compens	ation- voltage regulation - power factor c		n - pha	se bala	nce unsymmet	rical loa	ds.	
•	ations – increased power transfer capabil scillations - mitigations.	ity – sta	bility a	nd trar	isient limit – lo	sses – h	armoni	cs - sub
UNIT – III	Static Compensators and Components				Hours: 9			
	o conventional compensators - – synchr	onous d	conden	ser – s		reactor	– analv	sis and
	c compensators - TCR – TSC – SVC – TCSC –							
UNIT – IV	Design Of UPFC and Static Tap Changer		0		Hours: 9		-	
UPFC compon	ents – shunt devices - series devices –		on and	contro	ol – real and re	eactive	power ·	– UPFC
•	nd design philosophy. Conventional tap	•					•	
•	ferent schemes – comparison – specificati	-	-			•	0	0
UNIT – V	HVDC AND Static Generator Excitation		-		Hours: 9			
HVDC compon	ents - kinds of DC links – modern HVDC o	-		mmuta	ation issues - co	ontrol cł	haracter	ristics –
constant phase	e angle control – constant current and ex	tinction	angle o	control	- twelve and hi	igher pu	Ise ope	ration -
introduction to	o modern converters – protections - rea	active p	ower r	equire	ments – harmo	onics –	filtertyp	es and
design of vari	ious ac and dc filters. Solid state excit	ation o	f synch	nronou	s generators –	- differe	ent sche	emes –
Generator exc	itation systems – redundancy and reliabili	ty.						
Total contact I	Hours: 45 Total Tutorials: 15	Total F	Practica	al Class	es: Tot	tal Hour	rs:60	
Text Books:								
2. R.Moh – A Joł 3. K. R.F	T.J.E, Reactive power control in Electric synam and R.K.Varma, Thyristor-Based FACTS for Wiley and Sons, Inc. Publications.2002. Padiyar, HVDC Power Transmission Synational (p) Limited, New Delhi, 2003.	5 Contro	ollers fo	or Electi	rical Transmissi	on Syste		
Reference Boo	oks:							
1. Vijay k	K.Sood, HVDC and FACTS Controller: App	lication	of Stati	ic Conv	erters in powe	r syster	ns, IEEE	Power
Electro	onics and Power Systems series, Kluwer A	cademic	publis	hers, B	oston, 2004.			
	ni.G.Hingorani and Laszlo Gyugyi, Unders ress. 2000.	standing	g FACTS	S, IEEE	Power Engine	ering so	ociety s	consor,
	diyar, Facts Controllers in Power Transm hers, New Delhi, 2007.	ission a	nd Dist	ributio	n, New Age Int	ernatior	nal (P) L	imited,
4. "A Sta 1980,	tic alternative to the transformer on load	d tap ch	angingʻ	", IEEE	Trans. on PAS,	Vol.PAS	5-99, Jar	າ. /Feb.
•	ovements in Thyristor controlled static on S-101, Sept.1982, pp3091-3095.	- load ta	ip conti	rollers	for transformer	rs", IEEE	Trans.	on PAS,
	t Thyristor rectifiers for the Generator ugust, 1977, pp1219-1225.	Excitat	ion sy	stems"	, IEEE Trans.	On PAS	. Vol.P/	4S -96,

Semester :	Electrical and Electronics Engineering	FIUgia	mme:	VI. Lech	. (Electrical	Drives and	Control)
		Catego	ory : ⁻	ΓY				
Subject Code	Subject	Hou	rs / W	eek	Credit	Max	imum N	1arks
Subject Code	Subject	L	Т	Ρ	C	CA	SE	ТМ
EEE63	Power Quality	3	1	0	4	40	60	100
Prerequisite								
	• To study the power quality prob	lems in	grid co	nnecte	d system an	d isolated s	ystems	
Objectives	 To study the various power qua 	lity impr	oveme	nt tech	niques.			
	• To study about the various harn	nonics el	iminat	ion me	thods.			
	 Ability to apply knowledge or 	f powei	· quali	ty and	harmonics	in power	systen	ns and
	engineering to the analysis and	design o	f electi	rical cir	cuits			
Outcome	 Ability to design a system, com 	ponents	or pro	ocess to	o meet desi	red needs v	within r	ealistic
Outcome	constraints and to mitigate PQ	problem	s such	as ecoi	nomic, envii	ronmental,	social, e	ethical,
	health and safety.							
	Ability to function on multi-disc	iplinary t	teams.					
UNIT – I	Introduction				Hours: 9			
Introduction -0	Characterization of Electric Power Qual	ity: Trar	isients,	short	duration a	nd long du	ration	voltage
variations, Vol	tage Imbalance, waveform distortion,	Voltage	fluctu	ations,	Power fre	quency va	riation,	Power
acceptability cu	arves – power quality problems: poor loa	d power	factor	, Non li	near and un	balanced lo	bads, DO	C offset
in loads, Notch	ing in load voltage, Disturbance in supply	voltage	- Powe	r qualit	y standards			
UNIT – II	Non-Linear Loads				Hours: 9			
Single phase st	tatic and rotating AC/DC converters, Th	ree pha	se stat	ic AC/I	DC converte	ers, Battery	charge	ers, Arc
furnaces, Fluor	escent lighting, pulse modulated devices	, Adjusta	ble spe	ed driv	/es.			
UNIT – III	Measurement and Analysis Methods				Hours: 9			
Voltage, Currer	nt, Power and Energy measurements, po	wer fact	or mea	sureme	ents and de	finitions, ev	ent rec	orders,
	Error - Analysis: Analysis in the periodi	-					uency d	domain
methods: Lapla	ice's, Fourier and Hartley transform - The			rm - W	avelet Trans	sform.		
UNIT – IV	Analysis and Conventional Mitigation N				Hours: 9			
• •	wer outages, Analysis of unbalance: Sym		•		•	quantities,	Instant	aneous
symmetrical co	mponents, Instantaneous real and reacti	ve powe	ers. Ana	lysis of	distortion.			
•	•	•		•				
On-line extract	ion of fundamental sequence componen	ts from	measu		•			•
On-line extract voltage sag: D	ion of fundamental sequence componen etorit Edison sag score, Voltage sag en	ts from ergy, Vo	measu oltage	Sag Los	st Energy Ir	dex (VSLEI) - Ana	lysis of
On-line extract voltage sag: Do voltage flicker,	ion of fundamental sequence componen etorit Edison sag score, Voltage sag en Reduced duration and customer impac	ts from ergy, Vo t of out	measu oltage ages, C	Sag Los lassical	st Energy Ir I load balan	idex (VSLEI cing proble) - Ana	lysis of
On-line extract voltage sag: D voltage flicker, balancing, Clos	ion of fundamental sequence componen etorit Edison sag score, Voltage sag en Reduced duration and customer impac ed loop balancing current balancing, Han	ts from ergy, Vo t of out	measu oltage ages, C	Sag Los lassical	st Energy Ir I load balan age sag redu	idex (VSLEI cing proble) - Ana	lysis of
On-line extract voltage sag: D voltage flicker, balancing, Clos UNIT – V	ion of fundamental sequence component etorit Edison sag score, Voltage sag en Reduced duration and customer impac ed loop balancing current balancing, Har Power Quality Improvement	ts from ergy, Vo t of out monic re	measu oltage ages, C ductio	Sag Los lassical n, Volta	st Energy Ir l load balan age sag redu Hours: 9	dex (VSLEI cing proble ction.) - Ana em: Ope	lysis of
On-line extract voltage sag: D voltage flicker, balancing, Clos UNIT – V Utility-Custome	ion of fundamental sequence component etorit Edison sag score, Voltage sag en Reduced duration and customer impact ed loop balancing current balancing, Harr Power Quality Improvement er interface -Harmonic filters: passive, Ac	ts from ergy, Vo t of out monic re tive and	measu oltage ages, C ductio hybrid	Sag Los lassical n, Volta filters	st Energy In load balan age sag redu Hours: 9 -Custom po	dex (VSLEI cing proble ction. wer devices) - Ana em: Ope	lysis of en loop
On-line extract voltage sag: D voltage flicker, balancing, Clos UNIT – V Utility-Custome Network record	ion of fundamental sequence component etorit Edison sag score, Voltage sag en Reduced duration and customer impact ed loop balancing current balancing, Harr Power Quality Improvement er interface -Harmonic filters: passive, Ac ofiguring Devices, Load compensation	ts from ergy, Vo t of out monic re tive and using D	measu oltage s ages, C ductio hybrid oSTATC	Sag Los lassical n, Volta filters OM Vo	st Energy Ir I load balan age sag redu Hours: 9 -Custom po Dltage regu	dex (VSLEI cing proble ction. wer devices lation usin) - Ana em: Ope s: g DSTA	lysis of en loop
On-line extract voltage sag: Do voltage flicker, balancing, Clos UNIT – V Utility-Custome Network recor protecting sen	ion of fundamental sequence component etorit Edison sag score, Voltage sag en Reduced duration and customer impact ed loop balancing current balancing, Harr Power Quality Improvement er interface -Harmonic filters: passive, Ac infiguring Devices, Load compensation sitive loads using DVR, UPQC –control	ts from ergy, Vo t of out monic re tive and using D strateg	measu oltage s, C ductio hybrid STATC ies: P~	Sag Los lassical n, Volta filters OM Vo	st Energy Ir I load balan age sag redu Hours: 9 -Custom po Dltage regu	dex (VSLEI cing proble ction. wer devices lation usin) - Ana em: Ope s: g DSTA	lysis of en loop
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Department :	Electrical and Electronics Engineering	Programme: M.Tech. (Electrical Drives and Control))	
Semester :		Catego	ory : ⁻	ГҮ				
Subject Code	Subject	Hou	irs / W	eek	Credit	Max	imum N	larks
Subject code		L	Т	Р	С	СА	SE	ТМ
EEE64	Wind Energy Conversion Systems	3	1	0	4	40	60	100
Prerequisite								
Objectives	 To study the wind energy conve To study the application of pow To study the concept of PV tech 	ver conve	erters fo	or wind	electric system		ofelect	ricity
Outcome	 Ability to investigate the perfor Ability to design the power cont 					d applica	ations	
UNIT – I	Introduction				Hours: 9			
Historical deve	elopments, state of art of wind energy	technolo	gy, tur	bine ra	ting, Indian sc	enario a	and wor	ldwide
	, present status and future trends.							
	d geographical variation in the wind reso							
	Irnal variations, Turbulence, Gust wind Sp	peeds –E	xtreme	e wind s	peeds – effect	of heigl	ht- wind	rose –
Power availabl								
UNIT – II	Aerodynamics of Wind Turbines				Hours: 9			
	of aerofoil; lift; drag; stall; actuator dis							
	effect of stall and blade, turbulence a	and wak	es - Si	te sele	ection - Wind	d speed	and di	rection
measurements								
	Performance of Wind Turbines				Hours: 9			
	and variable speed WT-HAWT –VAWT–		•					
	ber of blades -Blade profile –power reg			-		roi, yaw	/ contro	i, pitch
UNIT – IV	ng systems- –modeling of Wind turbines t Grid Connected Wind Energy Systems	IOI POWE	rsyste	m studi	Hours: 9			
-	rators - induction generator - equivale	nt circui	t offi	cioncy		oporati	ion of 2	nhaca
	erators - permanent magnet generator					•		
-	wer control -braking systems - grid integ	-	-	enerate			variable	speeu
	Stand-Alone Wind Energy Systems		Jucs.		Hours: 9			
	process- effect of excitation capaci	tance e	nuivale	nt circi		nd frea	uency	control
	ower flow studies power quality issue							
	e – Batteries – Fuel Cells					,	,	
Total contact I	Hours: 45 Total Tutorials: 15	Total F	ractica	l Classe	s: To	tal Hour	s:60	
Text Books:	L							
1. N.Bha	dra, D.Kastha and S. Banerjee, Wind el	ectrical	system	s, Oxfo	rd University F	ress, 20	05.	
2. S.Ahm	ed, Wind Energy: Theory and Practice, PH	HI,2010.						
Reference Boo	ks:							
1. Tony E sons, 2	Burton, David Sharpe, Nick Jenkins and E 2001	rvin Bos	sanyi,	Hand B	ook of Wind E	nergy, J	ohn Wil	ey and
	ed Stiebler, Wind Energy Systems for E	ectrical	Power	Gener	ation, Springe	r, 2008.		
	. Johnson, Prentice hall Inc., Englewood C							
4. L. Lfrei	ris, Wind energy conversion system, Pren	tice hall	(U.K) Li	td., 199	0.			

4. L. Lfreris, Wind energy conversion system, Prentice hall (U.K) Ltd., 1990.

Department :	Electrical and Electronics Engineering	Programme: M.Tech. (Electrical Drives and Control))
Semester :		Catego	ory : ⁻	ΓY				
Subject Code	Subject	Ηοι	irs / W	eek	Credit	Max	kimum N	1arks
Subject Code		L	Т	Р	С	CA	SE	ТМ
EEE65	Special Electrical Machines	3	1	0	4	40	60	100
Prerequisite								
Objectives	 Understand the Construction and phase machines. Understand the Construction motor and switched reluctance 	and pri	-					-
Outcome	 The students are expected to understand the principle, construction operation as performance characteristic of various special machines Students will be capable of carrying out projects work in the field for different analysis. 							
UNIT – I	Single Phase Machines	/ 0			Hours: 9		•	
Principles and	construction of split phase motors - Sl	haded p	ole mo	otor – R	epulsion r	notor - Un	versal m	notor –
	chronous single phase motor – Reluctance							
UNIT – II	Stepper Motors				Hours: 9			
Constructional	features - Principle of operation - Modes	s of excit	ation –	- Types	of motors ·	– Drive syst	ems and	l circuit
-	Stepper motor – Applications Dynamic ch	aracteris	stics.		1			
UNIT – III	Switched Reluctance Motors				Hours:			
	features - Principle of operation - To	rque pre	edictior	n – Pov	ver contro	llers - Cha	racteristi	ics and
control - Appli					1			
UNIT – IV	Permanent Magnet Brushless DC Moto				Hours: 9			
	in DC motors - Difference between med					•		-
	tor drives - Torque and EMF equation	on; loro	que-Spe	ed cha	aracteristic	s; Sensors	- Cont	rollers;
Applications. UNIT – V	Three Phase AC Machines				Hours: 9			
			Maging	+				
Phasor diagram	peration - Constructional features of Per m - characteristics - Vector control -Appli aracteristics-control-Application in wind fa	cations.	Princip	le and o	constructio			
Total conta	ct Hours: 45 Total Tutorials: 15	Tot	al Prac	tical Cla	asses:	Tota	Hours:	5 0
Text Books:								
	Viller, Brushless Permanent Magnet and I jo and S.Negamori, Permanent Magnet B							L989.
Reference Boo	oks:							
2. A. Hug 3. Kenjo, 4. I.J.Nag 5. Loi Le	carnley, Stepping Motors, A Guide to Moc ghes, Electric Motors and Drives, Affiliated Stepping Motors and their Microprocess grath & D.P.Kothari, Electrical Machines, T iLai, Tze Fun Chan, Distributed Generat hers, 2007	l East-wo or Contr ata McC	est Pvt. ol, Clar Graw Hi	, Ltd., N endon I II, 1999	Aadras,199 Press, Oxfo	0. ord, 1989.		
PUDIIS								

6. B.L.Theraja, A.K.Theraja, Electrical Technology, vol-II, AC & DC Machines, S.Chand & Company Ltd., 2005.